



TrueIDE Mode Operation of Cactus Technologies' Flash Memory Cards

White Paper CTWP007

Cactus Technologies Limited

Suite C, 15/F, Capital Trade Center
62 Tsun Yip Street, Kwun Tong
Kowloon, Hong Kong
Tel: +852-2797-2277
Email: sales@cactus-tech.com

Cactus USA

3112 Windsor Road
Suite A356
Austin, Texas 78703
Tel: +512-775-0746
Email: americas@cactus-tech.com

Cactus-Tech.com

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Introduction

Both PC Card and Compact Flash standards define a mode in which the card can operate as though it is a hard disk drive. This is called TrueIDE mode operation. For this mode, the pins of the card interface are redefined to match the signal usage in the ATA Specification. The following sections of this white paper show the modified pin mappings and example schematics of adapter circuits for both PC Cards and CF Cards.

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TrueIDE Mode for PC Cards

The following table shows the pin mapping for PC Cards in various modes.

Pin No.	PC Card Memory Mode	PC Card I/O Mode	TrueIDE Mode
1	GND	GND	GND
2	D3	D3	D3
3	D4	D4	D4
4	D5	D5	D5
5	D6	D6	D6
6	D7	D7	D7
7	CE1#	CE1#	CS0#
8	A10	A10	---
9	OE#	OE#	GND
10	---	---	---
11	A9	A9	---
12	A8	A8	---
13	---	---	---
14	---	---	---
15	WE#	WE#	---
16	Rdv/Bsv	IREQ	INTRQ
17	VCC	VCC	VCC
18	VPP	VPP	VPP
19	---	---	---
20	---	---	---
21	---	---	---
22	A7	A7	---
23	A6	A6	---
24	A5	A5	---

Pin No.	PC Card Memory Mode	PC Card I/O Mode	TrueIDE Mode
25	A4	A4	---
26	A3	A3	---
27	A2	A2	A2
28	A1	A1	A1
29	A0	A0	A0
30	D0	D0	D0
31	D1	D1	D1
32	D2	D2	D2
33	WP	IOIS16#	IOCS16#
34	GND	GND	GND
35	GND	GND	GND
36	CD1#	CD1#	GND
37	D11	D11	D11
38	D12	D12	D12
39	D13	D13	D13
40	D14	D14	D14
41	D15	D15	D15
42	CE2#	CE2#	CS1#
43	VS1#	VS1#	---
44	IORD#	IORD#	IORD#
45	IOWR#	IOWR#	IOWR#
46	---	---	---
47	---	---	---
48	---	---	---

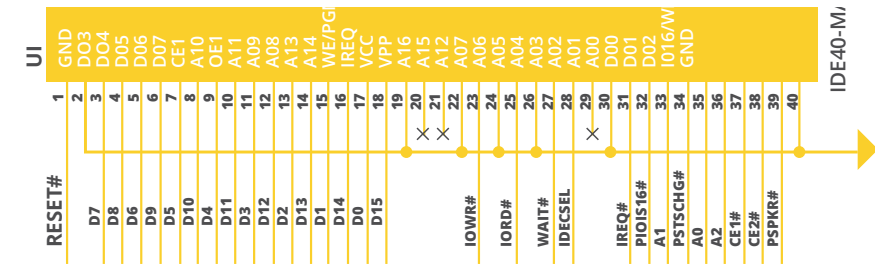
Pin No.	PC Card Memory Mode	PC Card I/O Mode	TrueIDE Mode
49	---	---	---
50	---	---	---
51	VCC	VCC	VCC
52	VPP	VPP	VPP
53	---	---	---
54	---	---	---
55	---	---	---
56	CSEL#	CSEL#	CSEL#
57	VS2#	VS2#	---
58	RESET	RESET	RESET

Pin No.	PC Card Memory Mode	PC Card I/O Mode	TrueIDE Mode
59	WAIT	WAIT	IORDY
60	INPACK#	INPACK#	DMARQ
61	REG#	REG#	DMACK#
62	BVD2	SPKR#	DASP#
63	BVD1	STSCHG#	PDIAG#
64	D8	D8	D8
65	D9	D9	D9
66	D10	D10	D10
67	CD2#	CD2#	GND
68	GND	GND	GND

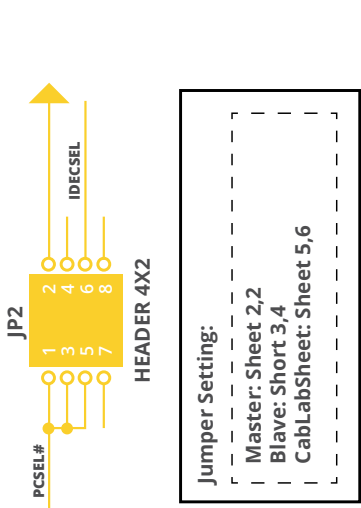
TABLE 1. PC CARD PIN ASSIGNMENT

Note: The host should take care to make sure that unused pins (e.g. A10-A3) on the PC Card interface while in TrueIDE mode are not left floating.

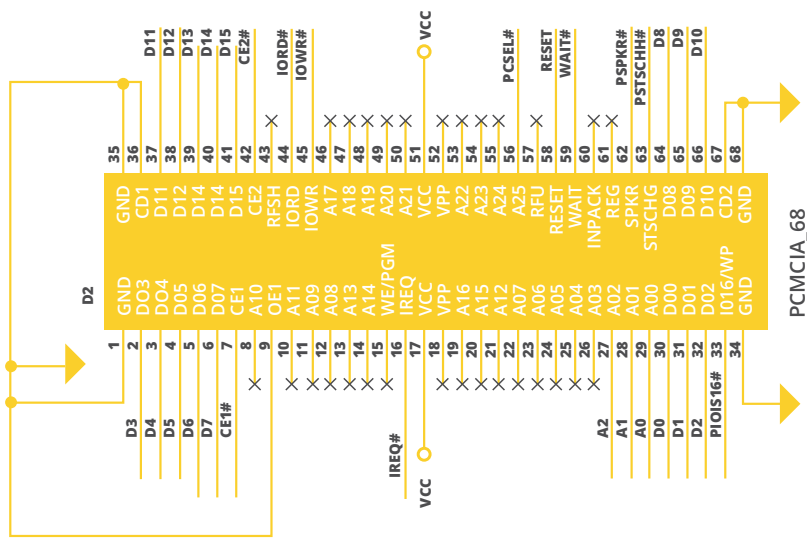
The next page shows a sample schematic of how to connect the pins from a PC Card socket to the 40 pin ATA connector on a typical host motherboard so that the PC Card can be use in TrueIDE mode.



40pin IDE connector. This is used for 3.5" di

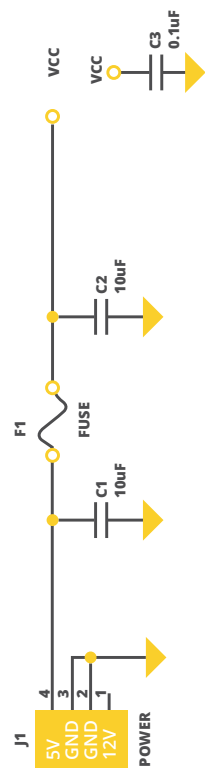


Optional drive activity LED.



68 pin PC Card connector.

4-pin PC power connector or any other appropriate power source from the host. Use of fuse is optional.



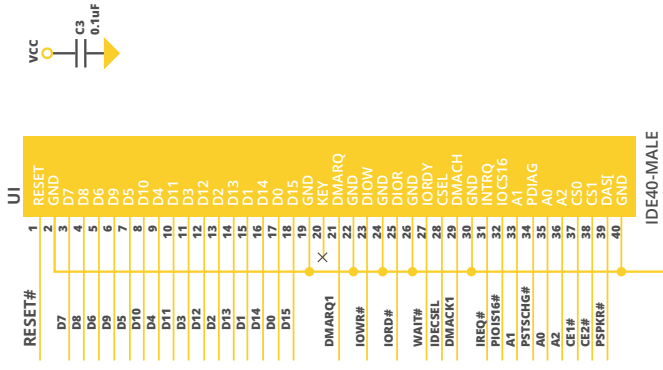
Pin No.	PC Card Memory Mode	PC Card I/O Mode	TrueIDE Mode
1	GND	GND	GND
2	D3	D3	D3
3	D4	D4	D4
4	D5	D5	D5
5	D6	D6	D6
6	D7	D7	D7
7	CE1#	CE1#	CS0#
8	A10	A10	---
9	OE#	OE#	GND
10	A9	A9	---
11	A8	A8	---
12	A7	A7	---
13	VCC	VCC	VCC
14	A6	A6	---
15	A5	A5	---
16	A4	A4	---
17	A3	A3	---
18	A2	A2	A2
19	A1	A1	A1
20	A0	A0	A0
21	DO	DO	DO
22	D1	D1	D1
23	D2	D2	D2
24	WP	IOIS16#	IOIS16#
25	CD2#	CD2#	---

Pin No.	PC Card Memory Mode	PC Card I/O Mode	TrueIDE Mode
26	CD1#	---	---
27	D11	D11	D11
28	D12	D12	D12
29	D13	D13	D13
30	D14	D14	D14
31	D15	D15	D15
32	CE2#	CE2#	CS1#
33	VS1#	VS1#	---
34	IORD#	IORD#	IORD#
35	IOWR#	IOWR#	IOWR#
36	WE#	WE#	---
37	Rdv/Bsv	IREQ	INTRQ
38	VCC	VCC	VCC
39	CSEL#	CSEL#	CSEL#
40	VS2#	VS2#	---
41	RESET	RESET	RESET
42	WAIT#	WAIT#	IORDY
43	INPACK#	INPACK#	DMARQ
44	REG#	REG#	DMACK#
45	BVD2	SPKR#	DASP#
46	BVD2	STSCHG#	SPDIAG#
47	D8	D8	D8
48	D9	D9	D9
49	D10	D10	D10
50	GND	GND	GND

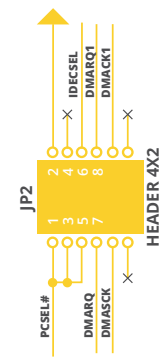
TABLE 2. CF CARD PIN ASSIGNMENT

Note: The host should take care to make sure that unused pins (e.g. A10-A3) on the PC Card interface while in TrueIDE mode are not left floating.

The next page shows a sample schematic of how to connect the pins from a CF Card socket to the 40pin ATA connector on a typical host motherboard so that the CF Card can be used in TrueIDE mode.

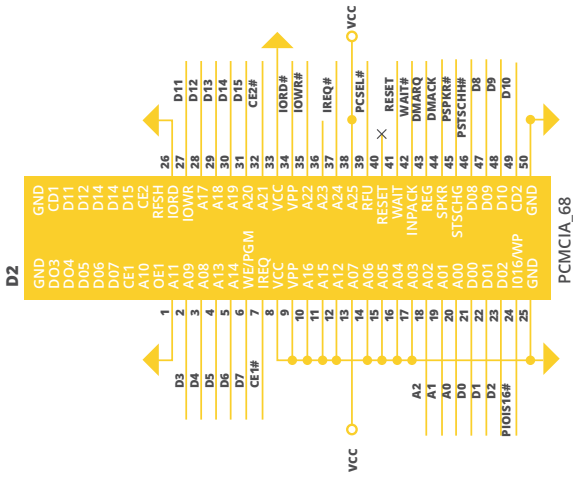


40pin IDE connector. This is used for 3.5"



Jumper Setting:

- Master: Sheet 1,2
- Slave: Short 3,4
- CableSheet: SHORT 5,6
- DMA SUPPORT: SHORT 7,8 AND 9,10



50 pin CompactFlash socket.

4-pin PC power connector or any other appropriate power source from the host. Use of fuse is optional.



Optional drive activity LED.

If you would like any additional information regarding data contained in this white paper feel free to contact a Cactus representative:

Asia/Pac RIM info@cactus-tech.com

Americas americas@cactus-tech.com

EMEA info@cactus-tech.com